

Q-040V

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Thomas J. Krutsick

Date of Deposit: February 1, 2002

Signature

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under 37 CFR 1.10 on the date indicated below and is addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

CASE

INVENTOR(S)

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TITLE

Method of Fabricating Complementary

Self-Aligned Bipolar Transistors

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

## **NEW APPLICATION UNDER 37 CFR §1.53(b)**

Enclosed are the following papers relating to the above-named application for patent:

Specification

16 Sheets of informal drawing(s)

1 Assignment with Cover Sheet

Declaration and Power of Attorney

Information Disclosure Statement with Form PTO-1449 and cited reference(s)

CLAIMS AS FILED				
	NO. FILED	NO. EXTRA	RATE	CALCULATIONS
Total Claims	15-20 =	0	x \$18 =	\$0
Independent Claims	3-3 =	0	x \$84 =	\$0
Multiple Dependent Claim(s), if applicable			\$280 =	\$0
Basic Fee				\$740
			TOTAL FEE:	\$740

Please file the application and charge **Agere Systems Deposit Account No. 50-1735** the amount of \$740, to cover the filing fee. Duplicate copies of this letter are enclosed. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-1735** as required to correct the error.

The Assistant Commissioner for Patents is hereby authorized to treat any concurrent or future reply, requiring a petition for extension of time under 37 CFR §1.136 for its timely submission, as incorporating a petition for extension of time for the appropriate length of time if not submitted with the reply.

Date: February 1, 2002

Please address all correspondence to: Ryan, Mason & Lewis, LLP, 90 Forest Avenue, Locust Valley, New York 11560. Telephone calls should be made to the undersigned attorney at (516) 759-7662.

Respectfully,

Wayne L. Ellenbogen

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